

FIG. 1

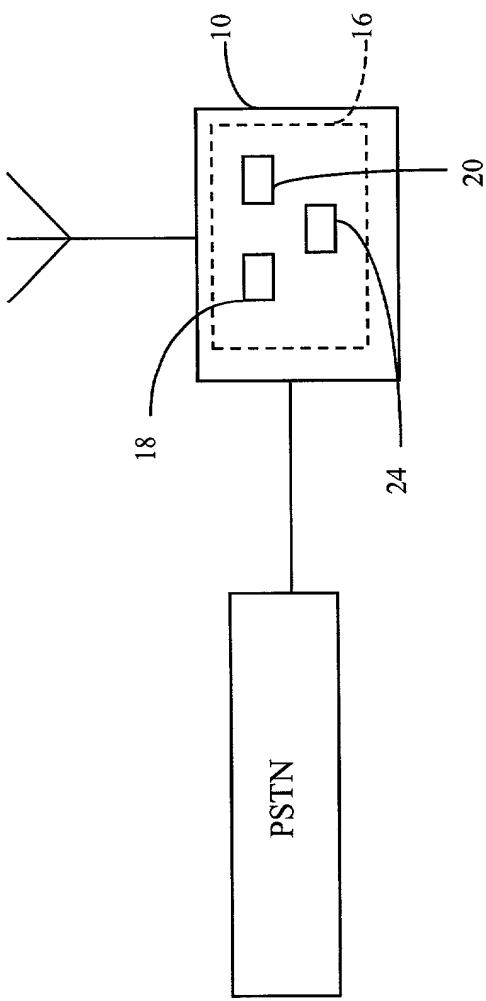
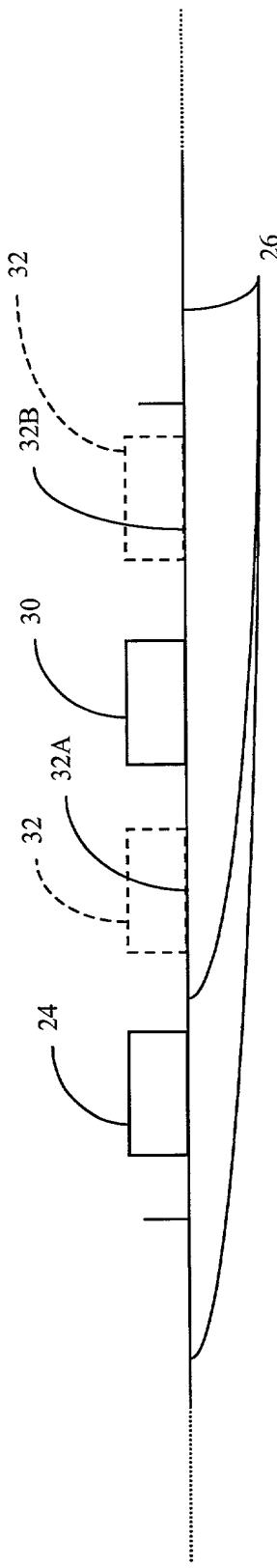


FIG. 2



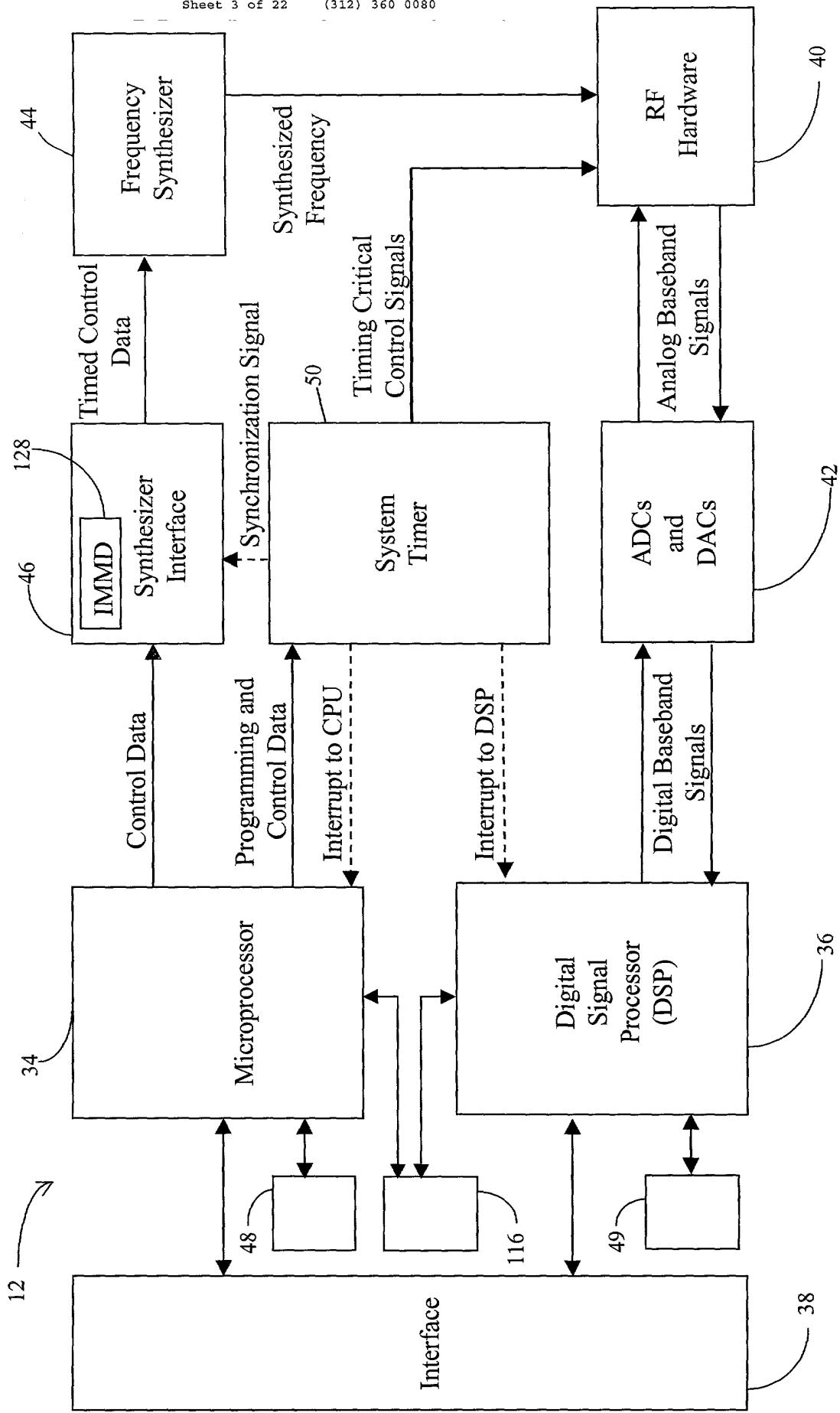


FIG. 4

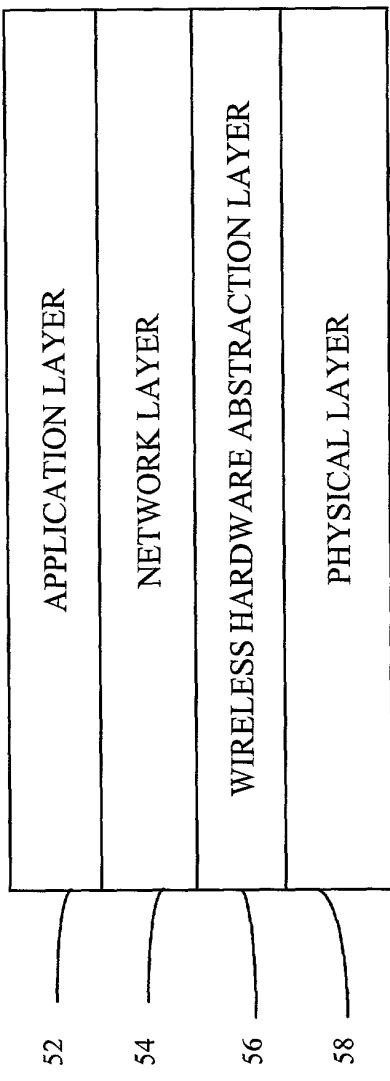


FIG. 5

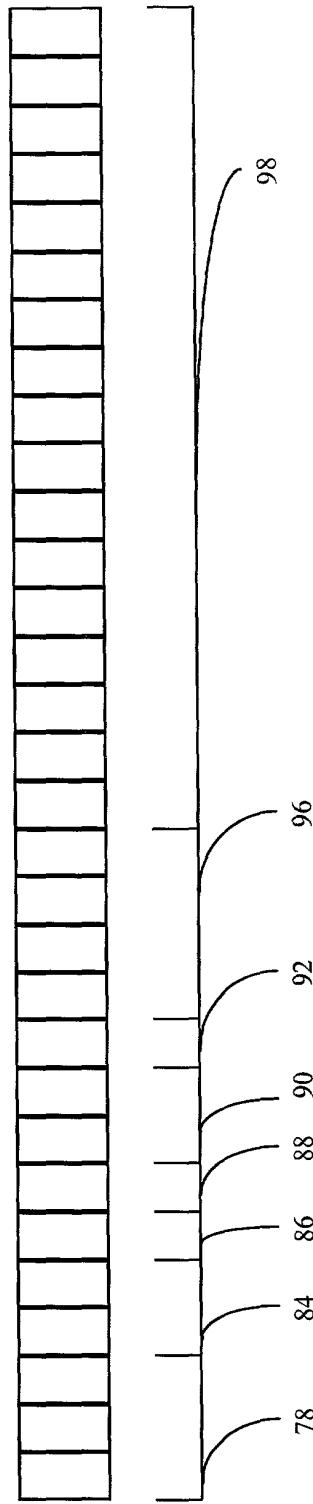


FIG. 7

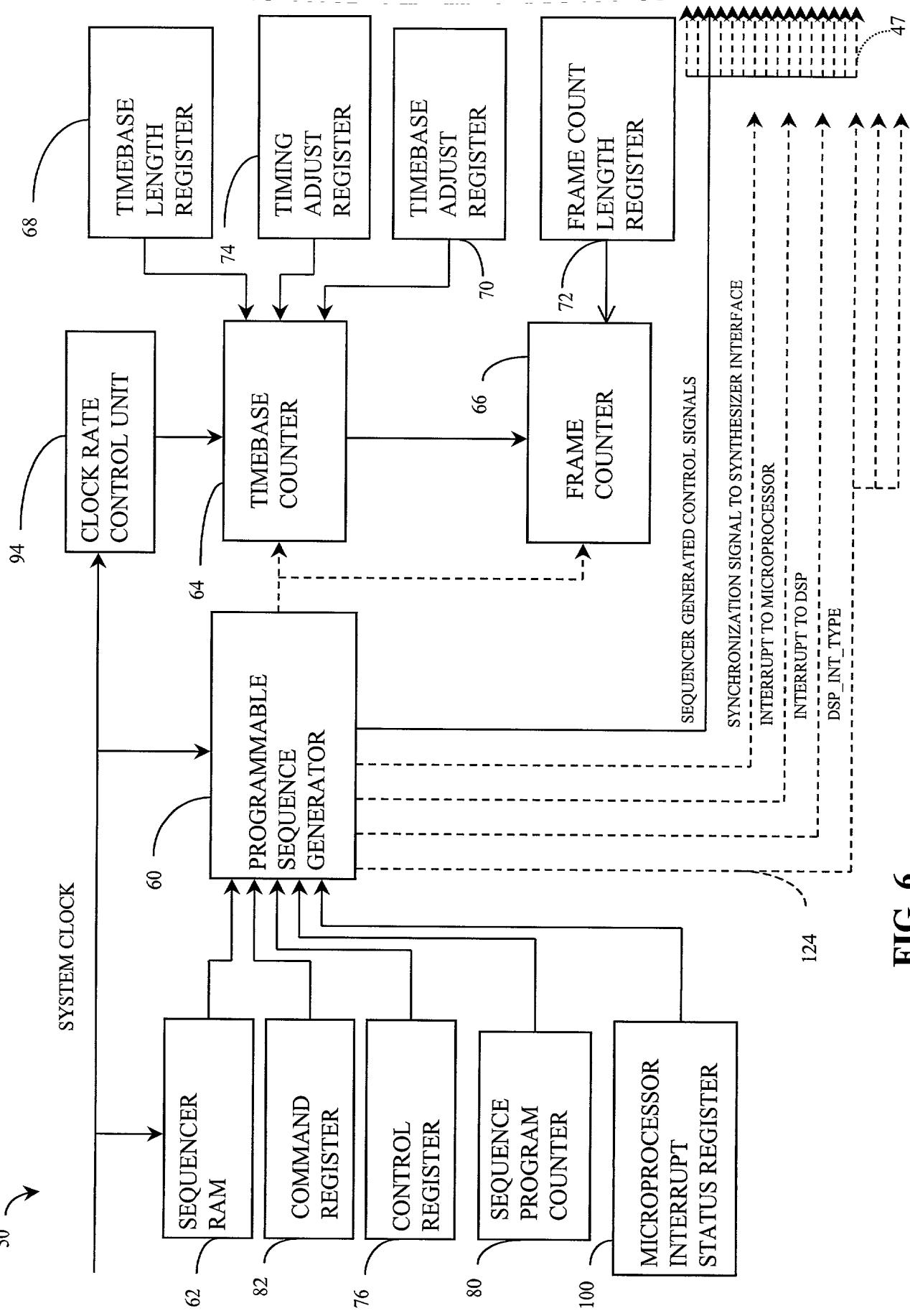


FIG. 6

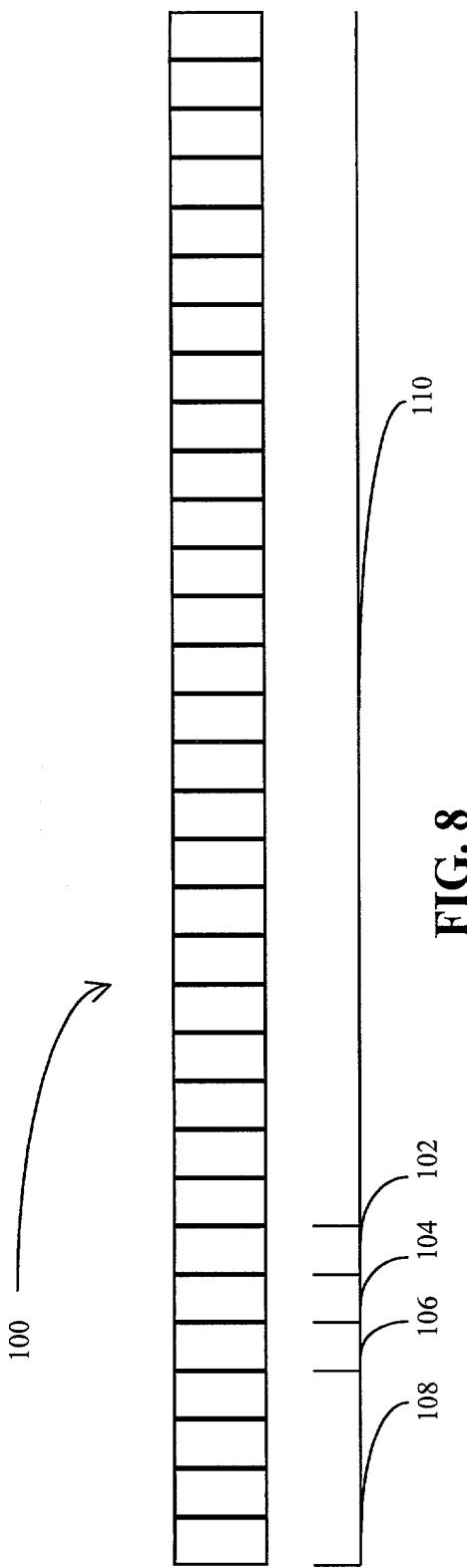


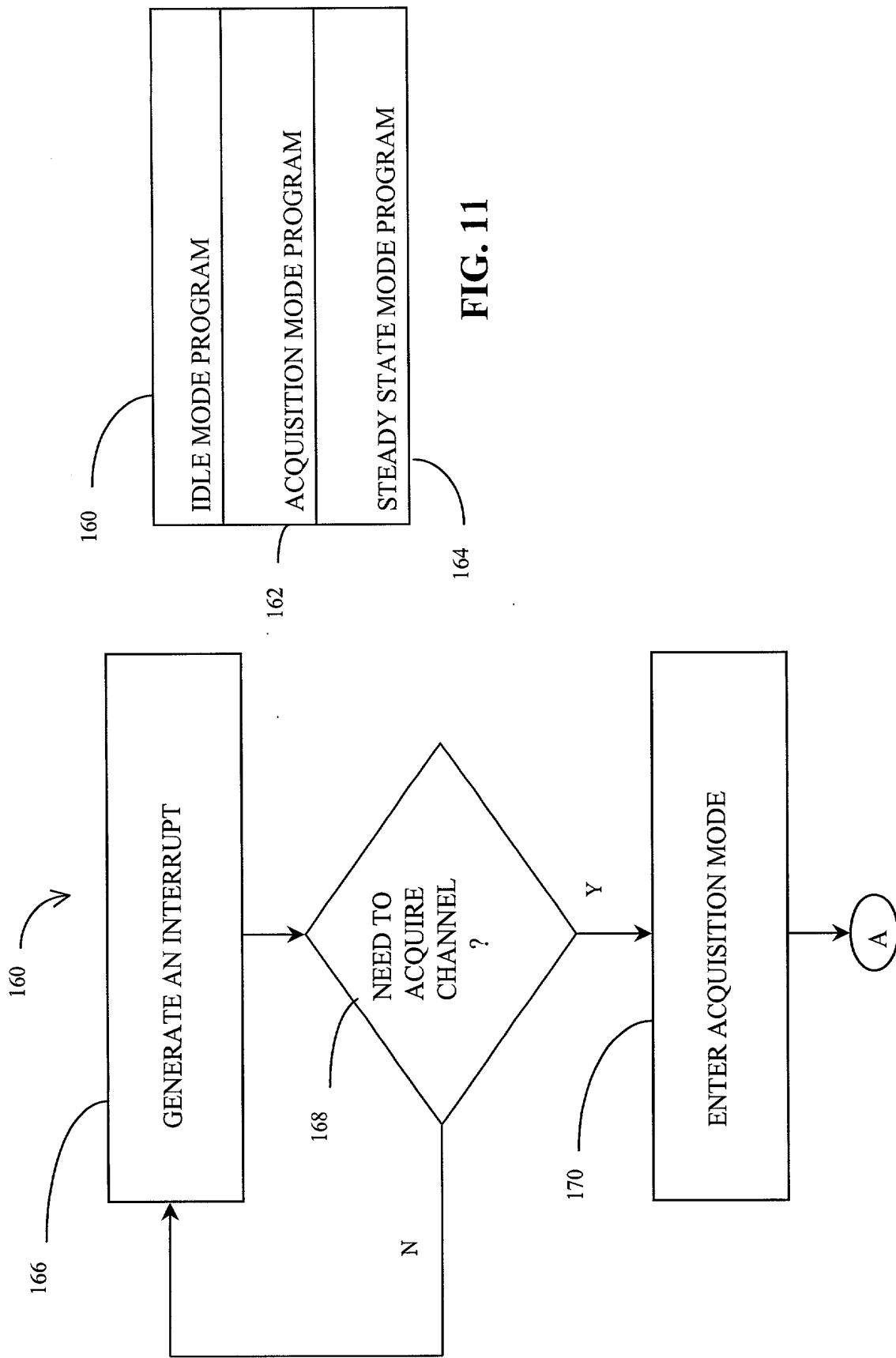
FIG. 8

TYPES OF DSP INTERRUPTS	
146	INVALID
148	FRAME
150	RXWIN
152	TXWIN
154	HANDOFF
156	APC
158	TXWINOFF
157	RESERVED

FIG. 10

	INSTRUCTION	FORMAT
112		
114	JCMD	JCMD aaaa aaaa
117	JMP	JMP ABCD aaaa aaaa
118	SET	SET m, k, j
120	CLR	CLR m, k, j
122	DSPINT	DSPINT tttt
126	ARMINT	ARMINT tttt
132	SYNSEND	SYNSEND
133	RXENA	RXENA
134	RXDIS	RXDIS
136	TXSTART	TXSTART
138	RTBWAIT	RTBWAIT w
140	FRWAIT	FRWAIT w
142	TBWAIT	TBWAIT w
144	TBADJSN	TBADJSN
	NOP	NOP

FIG. 9



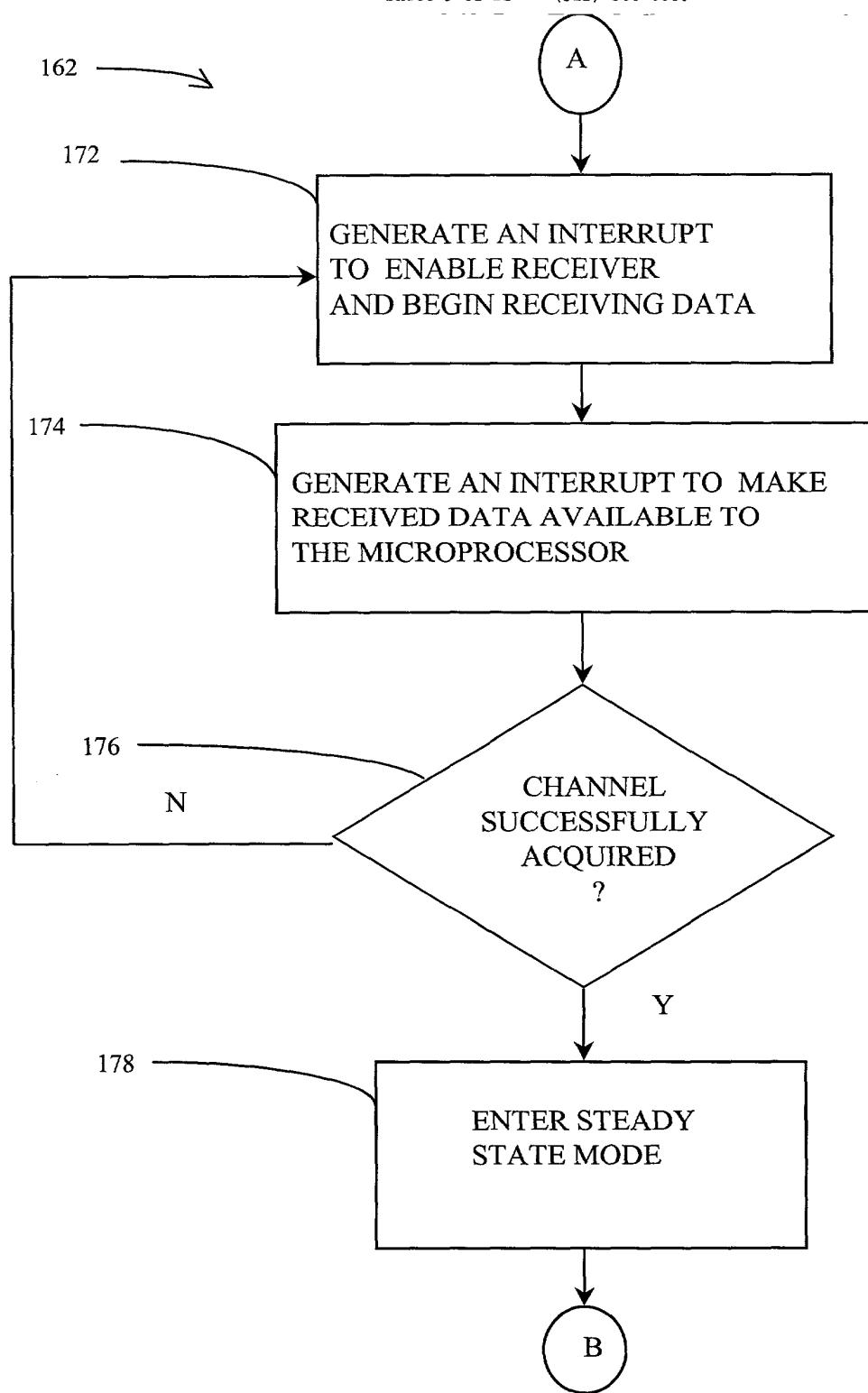


FIG. 13

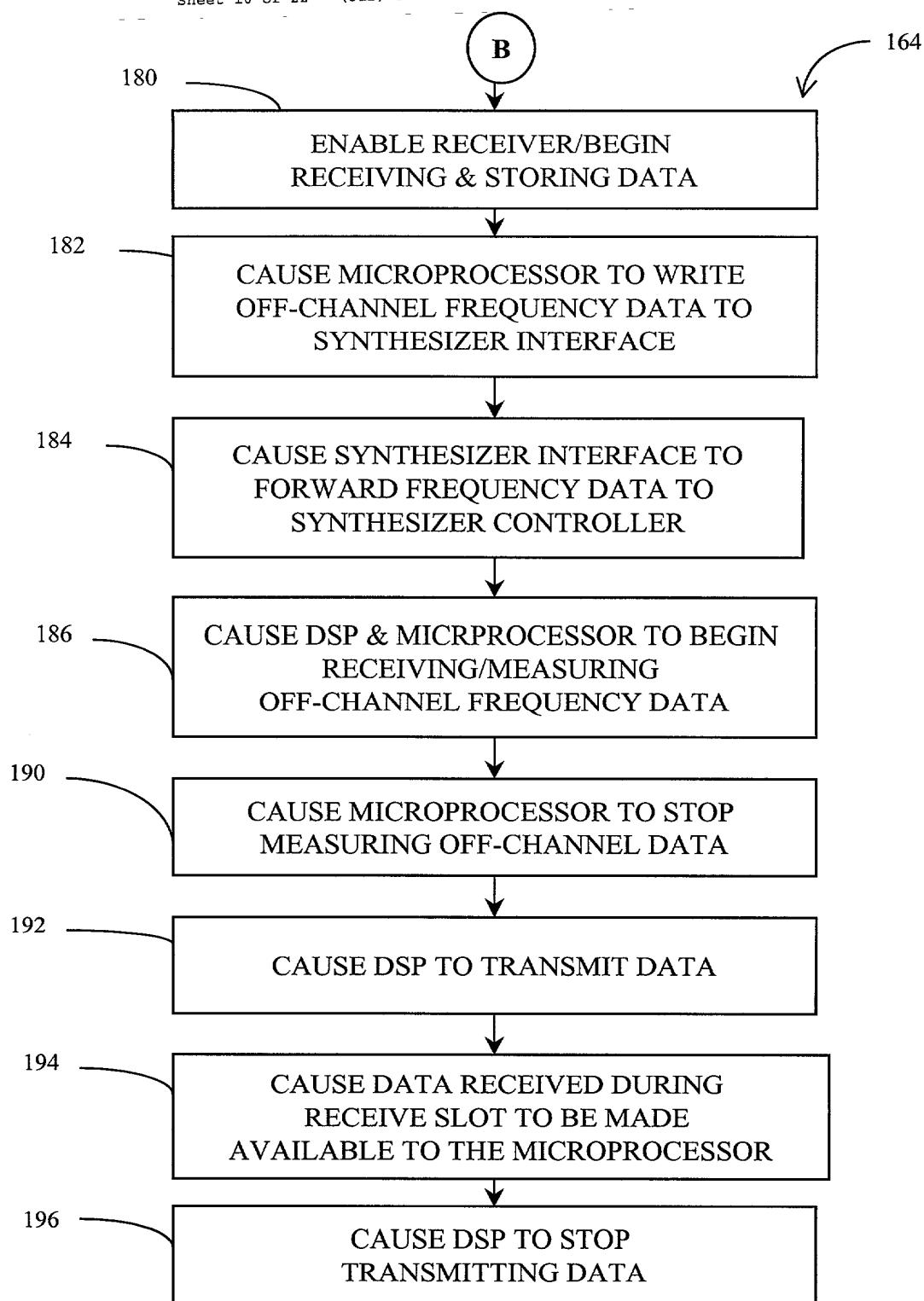


FIG. 14

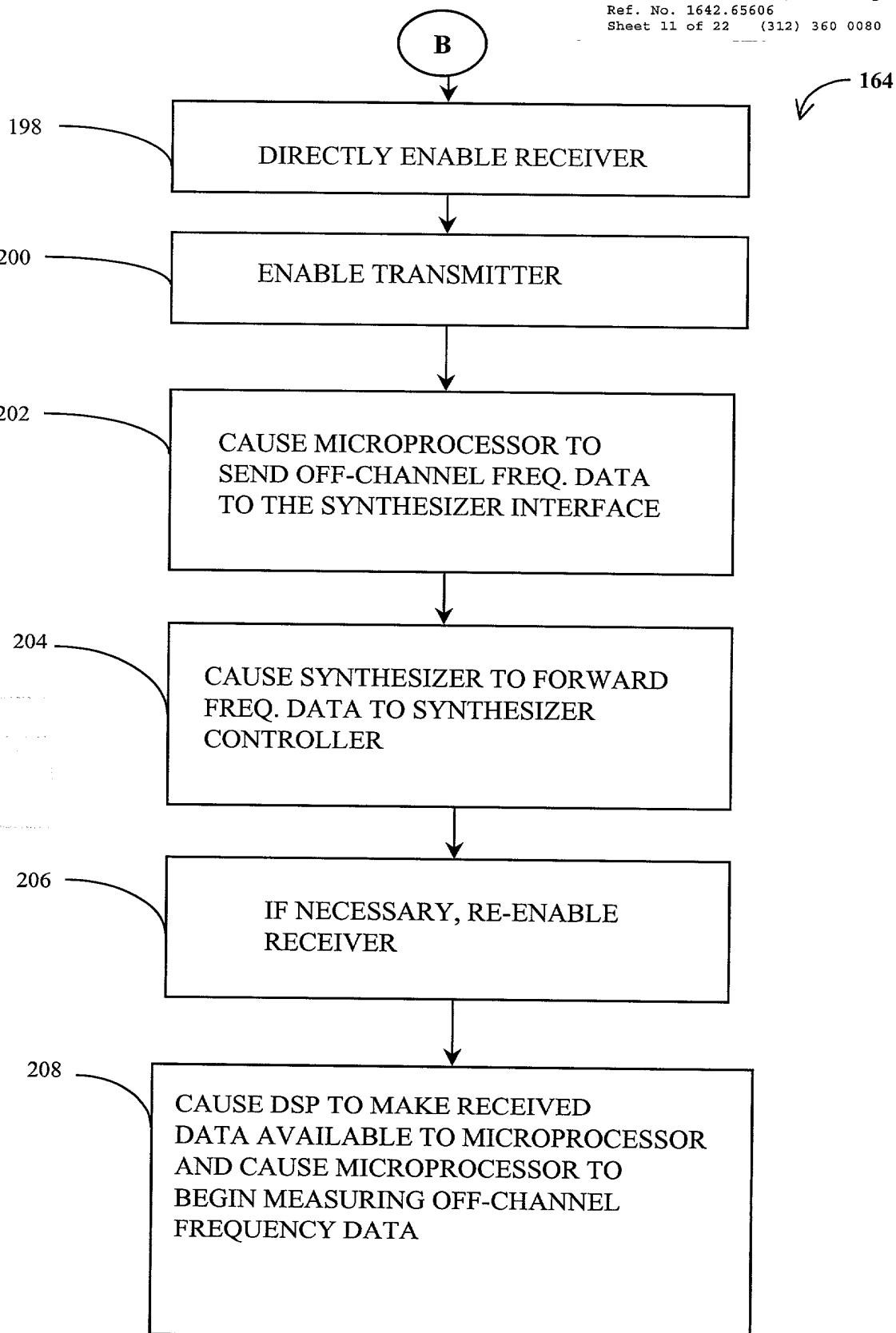


FIG. 15

FIG. 16

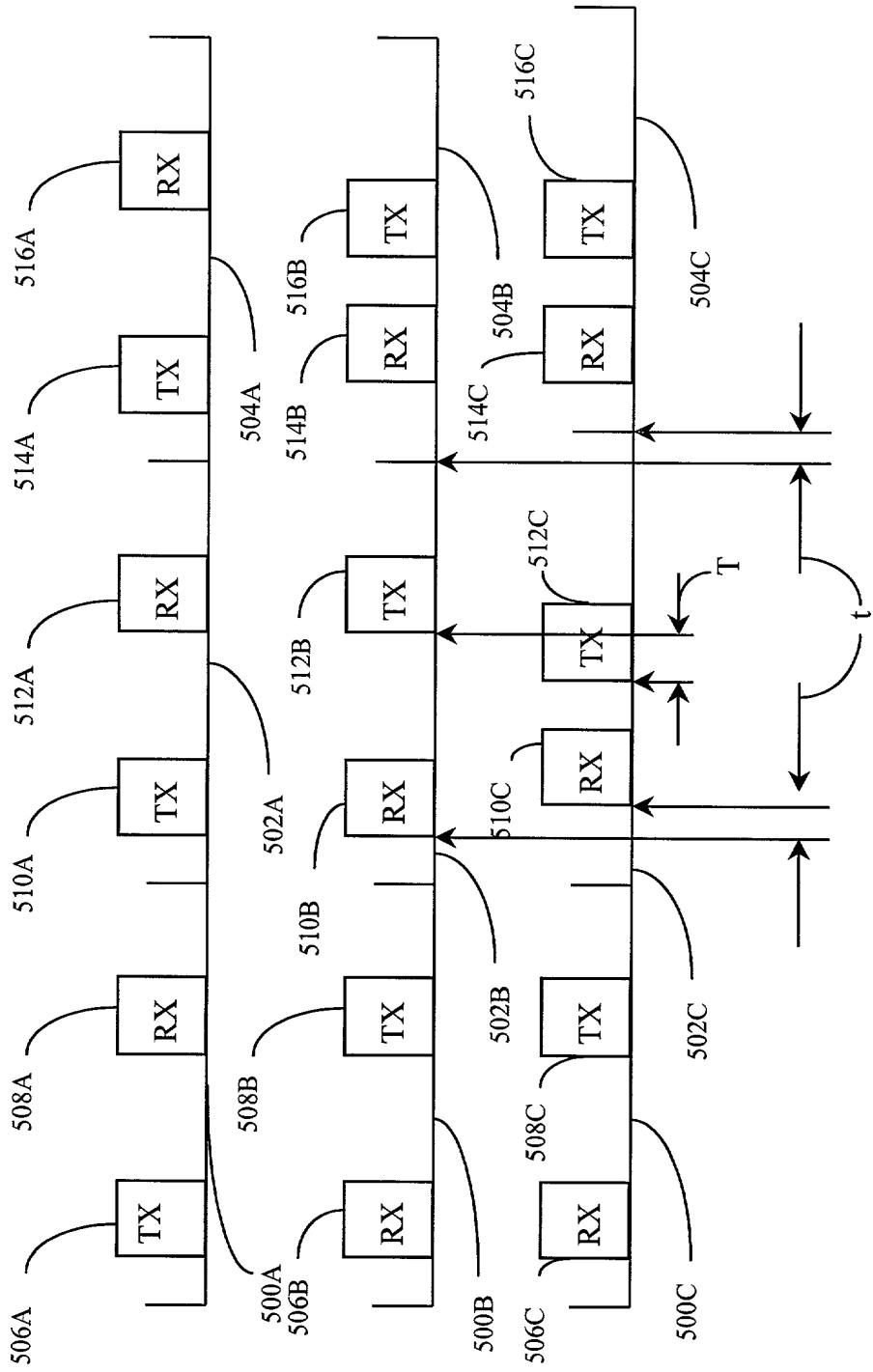
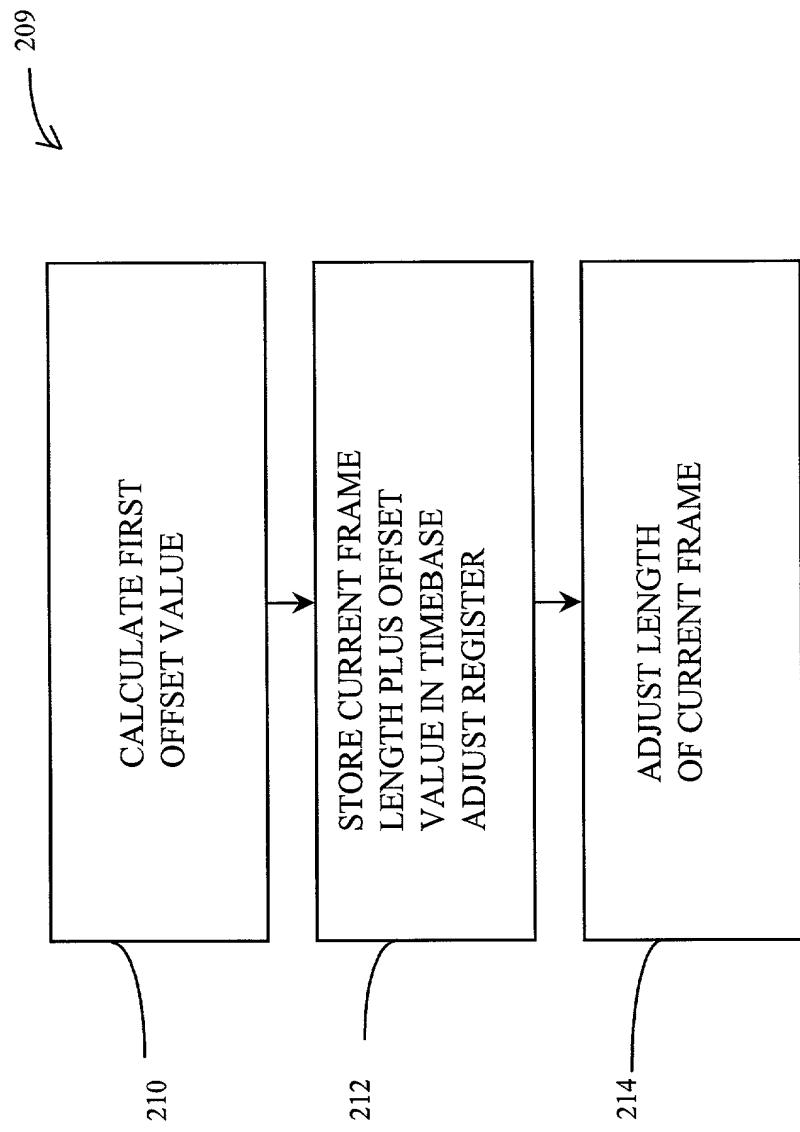


FIG. 17



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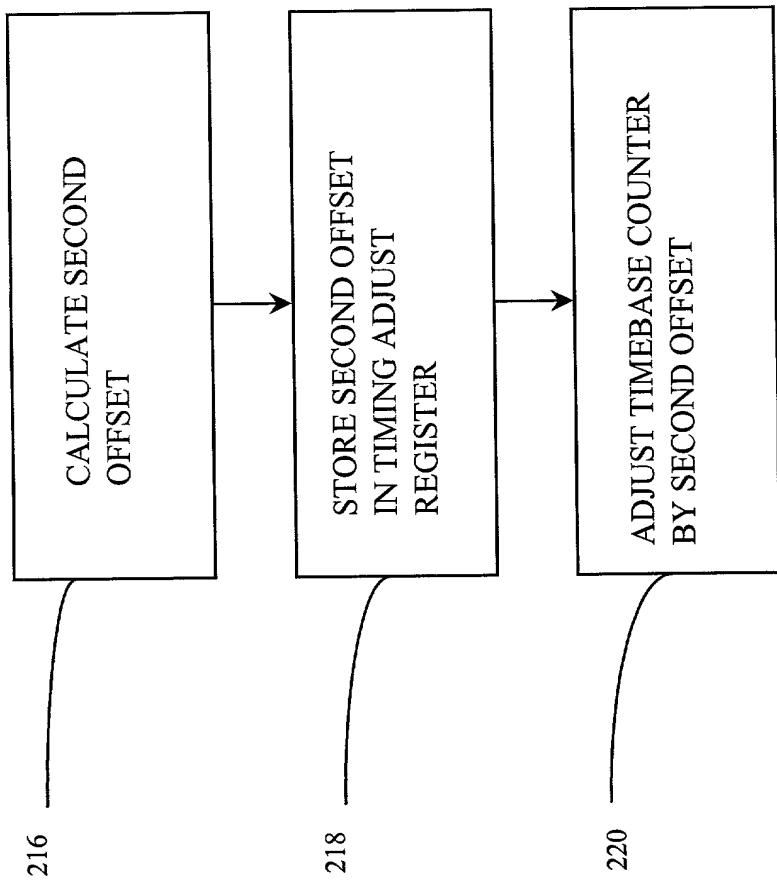


FIG. 18

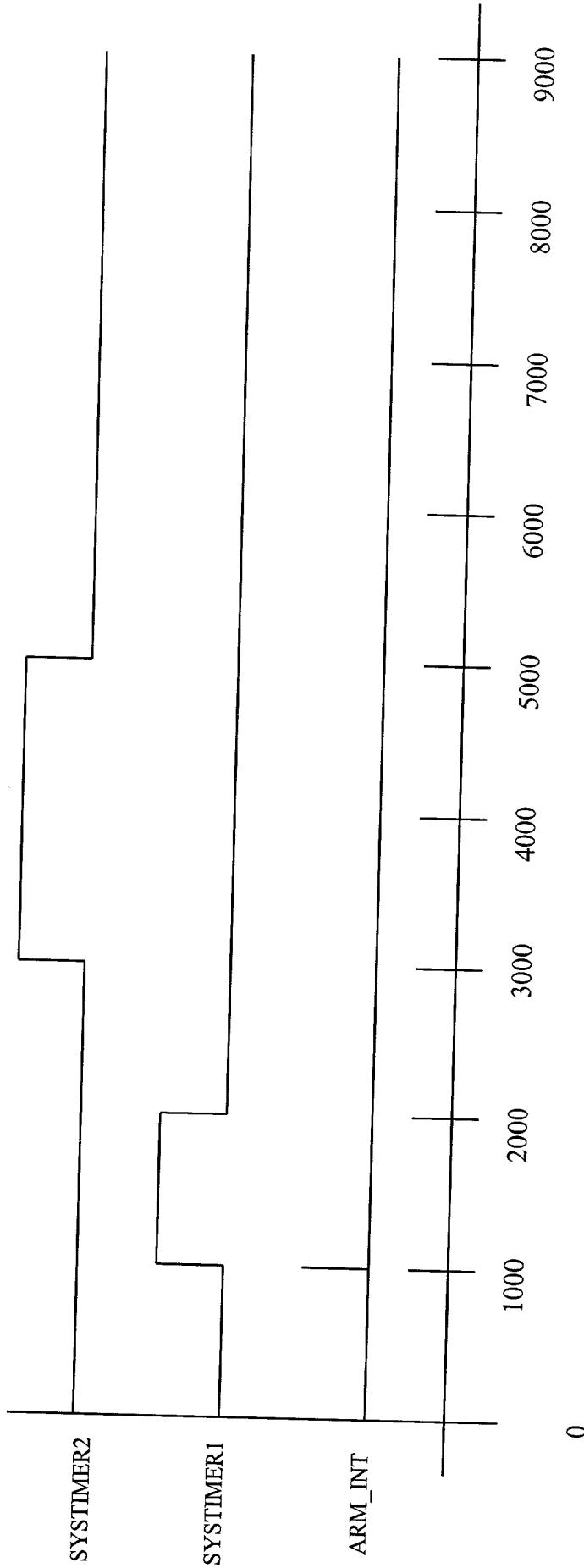


FIG. 19

SEQUENCER RAM ADDRESS	INSTRUCTION
0	TBWAIT 1000
1	ARMINT 3
2	SET 1,1,1
3	TBWAIT 2000
4	CLR 1,1,1
5	TBWAIT 3000
6	SET 2,2,2
7	TBWAIT 5000
8	CRL 2,2,2
9	JCMD 0

FIG. 20

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SEQUENCER RAM ADDRESS	INSTRUCTION	
0	JMP 1000, 6	246
1	TBWAIT 1000	248
2	ARMINT 3	250
3	SET 1, 1, 1	252
4	TBWAIT 2000	254
5	CLR 1, 1, 1	256
6	JMP 0100, 11	258
7	TBWAIT 3000	260
8	SET 2, 2, 2	262
9	TBWAIT 2000	264
10	CLR 2, 2, 2	266
11	JMP 0010, 16	268
12	TBWAIT 6000	270
13	SET 2, 2, 2	272
14	TBWAIT 6500	274
15	CLR 2, 2, 2	276
16	JMP 0001, 21	278
17	TBWAIT 7000	280
18	SET 2, 2, 2	282
19	TBWAIT 7500	284
20	CLR 2, 2, 2	286
21	JCMD 0	288

FIG. 21

TOP SECRET//~~REF ID: A6560~~

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SEQUENCER RAM ADDRESS	INSTRUCTION	
0	JCMD 1	292
1	TBWAIT 1000	294
2	ARMINT 3	296
3	SET 1, 1, 1	298
4	RTBWAIT 1000	300
5	JCMD 6	302
6	CLR 1, 1, 1	304
7	JCMD 8	306
8	TBADJSN 0	308
9	RTBWAIT 2000	310
10	SET 2, 2, 2	312
11	RTBWAIT 1000	314
12	JCMD 13	316
13	CLR 2, 2, 2	318
14	TBADJSN 1	320
15	JCMD 0	322

FIG. 22

FIRST PROGRAM	
0	TBWAIT 50
1	JCMD 0
RXSTART PROGRAM	
2	RTBWAIT 30
3	SET 1, 1, 1
4	RTBWAIT 20
5	RXENA
6	RTBWAIT 50
7	JCMD 0
RXEND PROGRAM	
8	RTBWAIT 50
9	RXDIS
10	RTBWAIT 20
11	CLR 1, 1, 1
12	RTBWAIT 30
13	JCMD 0
TXSTART PROGRAM	
14	RTBWAIT 29
15	SET 2, 2, 2
16	RTBWAIT 20
17	TXSTART
18	RTBWAIT 50
19	JCMD 0

FIG. 23A

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TXEND PROGRAM

20	RTBWAIT 70	372
21	CLR 2, 2, 2	374
22	RTBWAIT 30	376
23	JCMD 0	378
		380

REPEAT PROGRAM

24	RTBWAIT 100	382
25	JCMD 0	384
		386

LAST PROGRAM

26	RTBWAIT 50	388
27	JCMD 0	390
		392

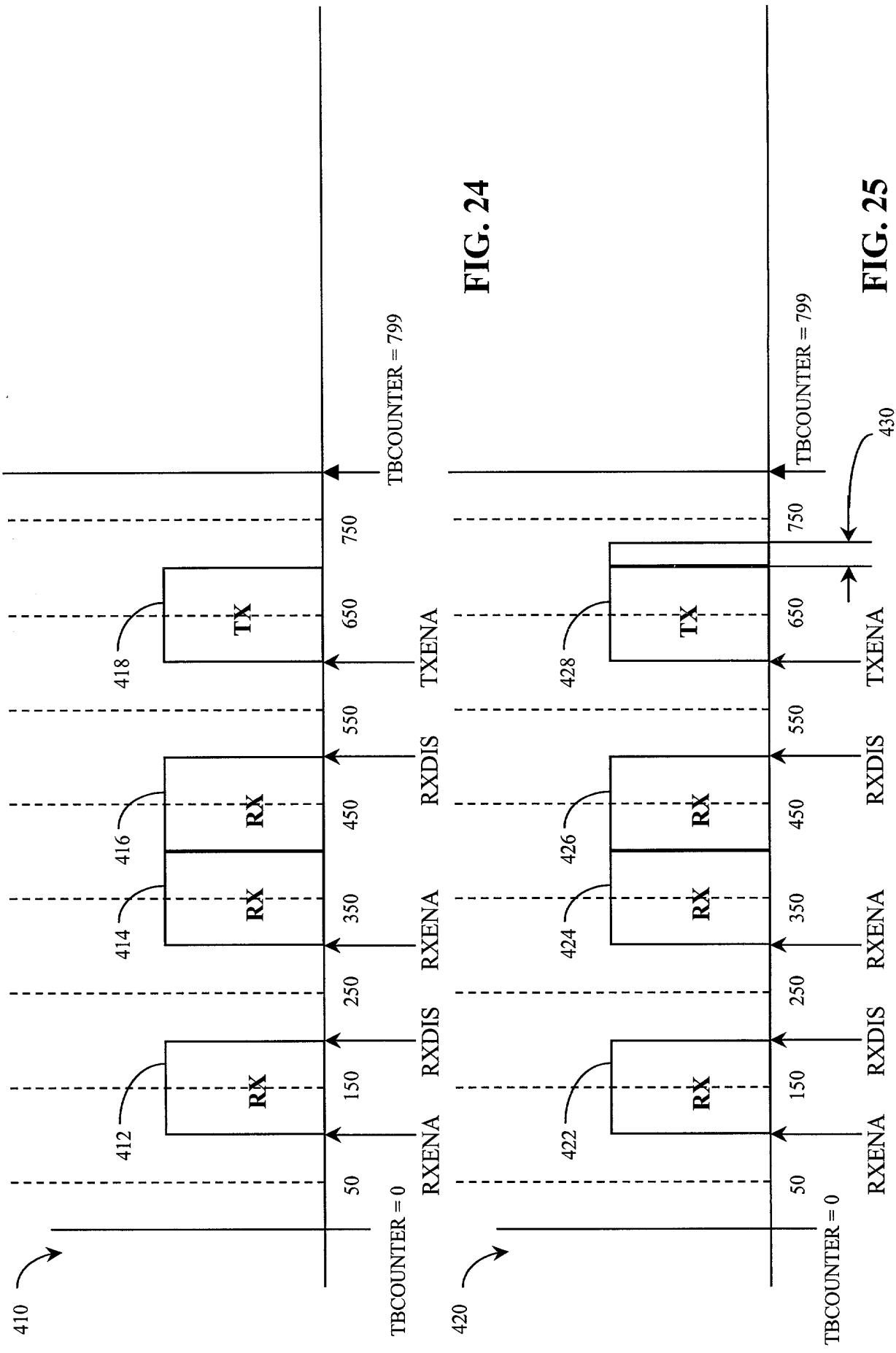
ADVANCE PROGRAM

28	TBADJSN 0	394
29	JCMD 0	396
		398

RETARD PROGRAM

30	TBADJSN 1	400
31	JCMD 0	402
		404

FIG. 23B



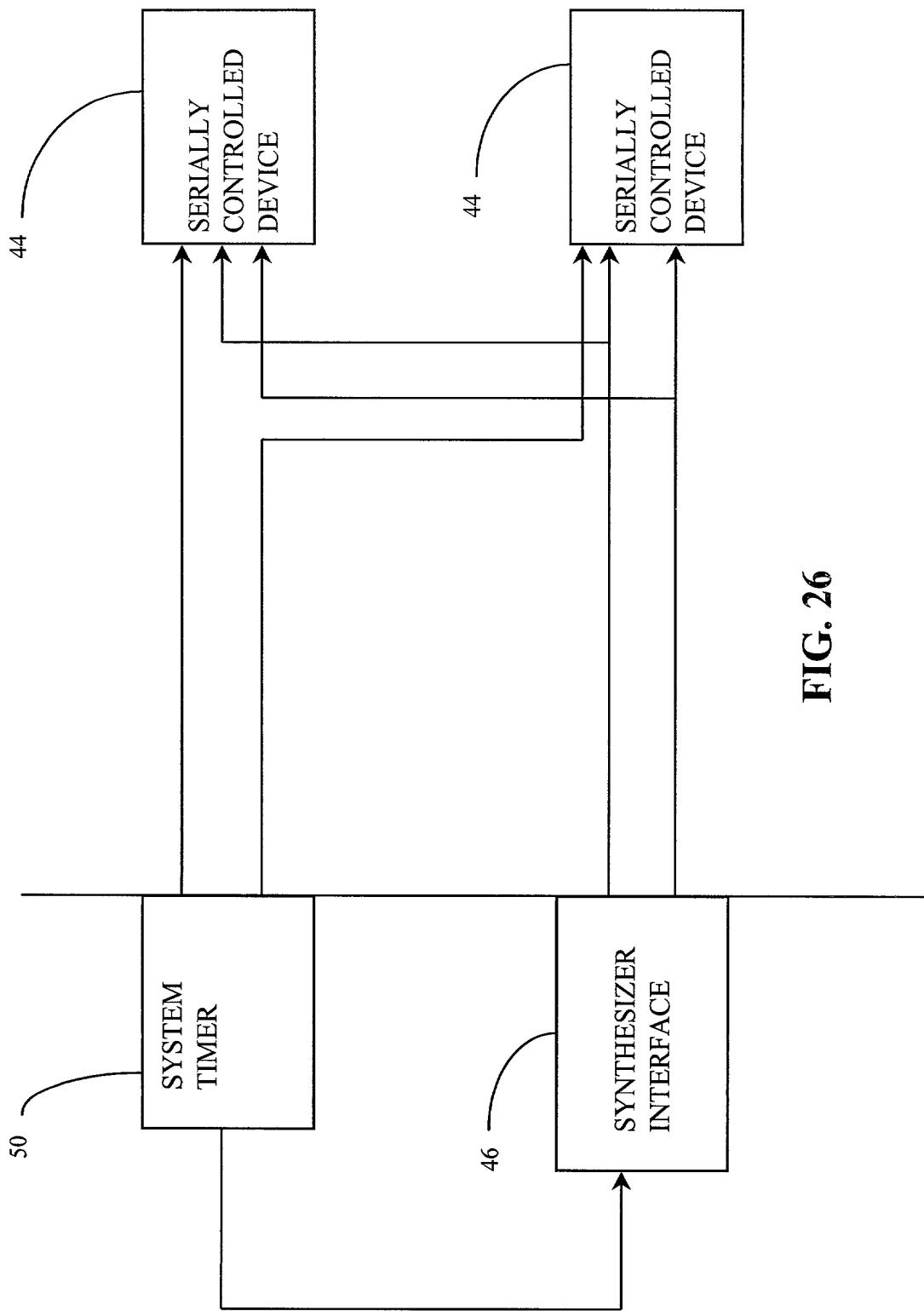


FIG. 26